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	Applicati n No.	Applicant(s)
N. C. of Allowahilita	10/604,554	KARTSCHOKE ET AL.
Notice of Allowability	Examin r	Art Unit
	My-Trang N. Ton	2816
The MAILING DATE of this communication apperature All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIOF of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in or other appropriate communication is s	n this application. If not included unication will be mailed in due course. THIS
1. 🔀 This communication is responsive to Admt. filed 12/17/04 a	and Tel. Int. 1/10/05.	
2. X The allowed claim(s) is/are 3-6,11-14,17-19 and 21-24.		
3. A The drawings filed on 30 July 2003 are accepted by the Ex	aminer.	
<ul> <li>4. Acknowledgment is made of a claim for foreign priority ur</li> <li>a) All b) Some* c) None of the:</li> <li>1. Certified copies of the priority documents have</li> <li>2. Certified copies of the priority documents have</li> </ul>	been received.	
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3. Copies of the certified copies of the priority do	cuments have been received	in this hational stage application from the
International Bureau (PCT Rule 17.2(a)).  * Certified copies not received:		
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		a reply complying with the requirements
5. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give		
<ol> <li>CORRECTED DRAWINGS (as "replacement sheets") must</li> <li>(a) ☐ including changes required by the Notice of Draftspers</li> <li>1) ☐ hereto or 2) ☐ to Paper No./Mail Date</li> <li>(b) ☐ including changes required by the attached Examiner's</li> </ol>	on's Patent Drawing Reviev	•
Paper No./Mail Date  Identifying indicia such as the application number (see 37 CFR 1		
each sheet. Replacement sheet(s) should be labeled as such in t	he header according to 37 CF	R 1.121(d).
<ol> <li>DEPOSIT OF and/or INFORMATION about the depo- attached Examiner's comment regarding REQUIREMENT.</li> </ol>	SIT OF BIOLOGICAL MATE FOR THE DEPOSIT OF BIO	ERIAL must be submitted. Note the DLOGICAL MATERIAL.
Attachment(s)	_	
1. Notice of References Cited (PTO-892)		formal Patent Application (PTO-152)
2. Notice of Draftperson's Patent Drawing Review (PTO-948)		ummary (PTO-413), Mail Date <u>01/10/05</u> .
Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date	8), 7. 🗵 Examiner's	Amendment/Comment
4. Examiner's Comment Regarding Requirement for Deposit	8. 🛛 Examiner's	Statement of Reasons for Allowance
of Biological Material	9. 🗌 Other	- myfraighn
		MY-TRANG NU TON PRIMARY EXAMINER
		1/10/05

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## **EXAMINER'S AMENDMENT**

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Att. Henkler on January 10, 2005.

The application has been amended as follows:

In claim 11, line 2, replace "second" (both occurrence) with – first --.

## Reasons for allowance

The following is an examiner's statement of reasons for allowance:

None of the prior art disclosed or suggested to show the particular structure and/or the particular operation recited in these claims namely: the combination of "a first inverter circuit ..., the first NFET having a body and drain being coupled one to another, and the first PFET having a body and drain coupled one to another", "a second inverter circuit ..., the second NFET having a body and a drain coupled one to another, and the second PFET having a body and drain coupled one to another" and "wherein the body of at least one of the first NFET ... a soft error event at the cross-coupled inverter" as recited in claim 22; the combination of "a first inverter circuit ..., the first NFET having a body and drain being coupled one to another with a first capacitor, and the first PFET having a body and drain coupled one to another with a second capacitor", "a second inverter circuit ..., the second NFET having a body and a drain coupled one

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to another with a third capacitor; and the second PFET having a body and drain coupled one to another with a fourth capacitor" and "wherein the body of at least one of the first NFET ... in response to a soft error event at the cross-coupled inverter" as recited in claim 23; the combination of all the method steps as recited in claim 24.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to My-Trang N. Ton whose telephone number is 571-272-1754. The examiner can normally be reached on 7:00 a.m - 5:30 p.m.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Timothy Callahan can be reached on 571-272-1740. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

> My-Trang N. Ton Primary Examiner